

南亞電路板股份有限公司

NAN YA PRINTED CIRCUIT BOARD CORPORATION

COMPANY BRIEFING

March 2010

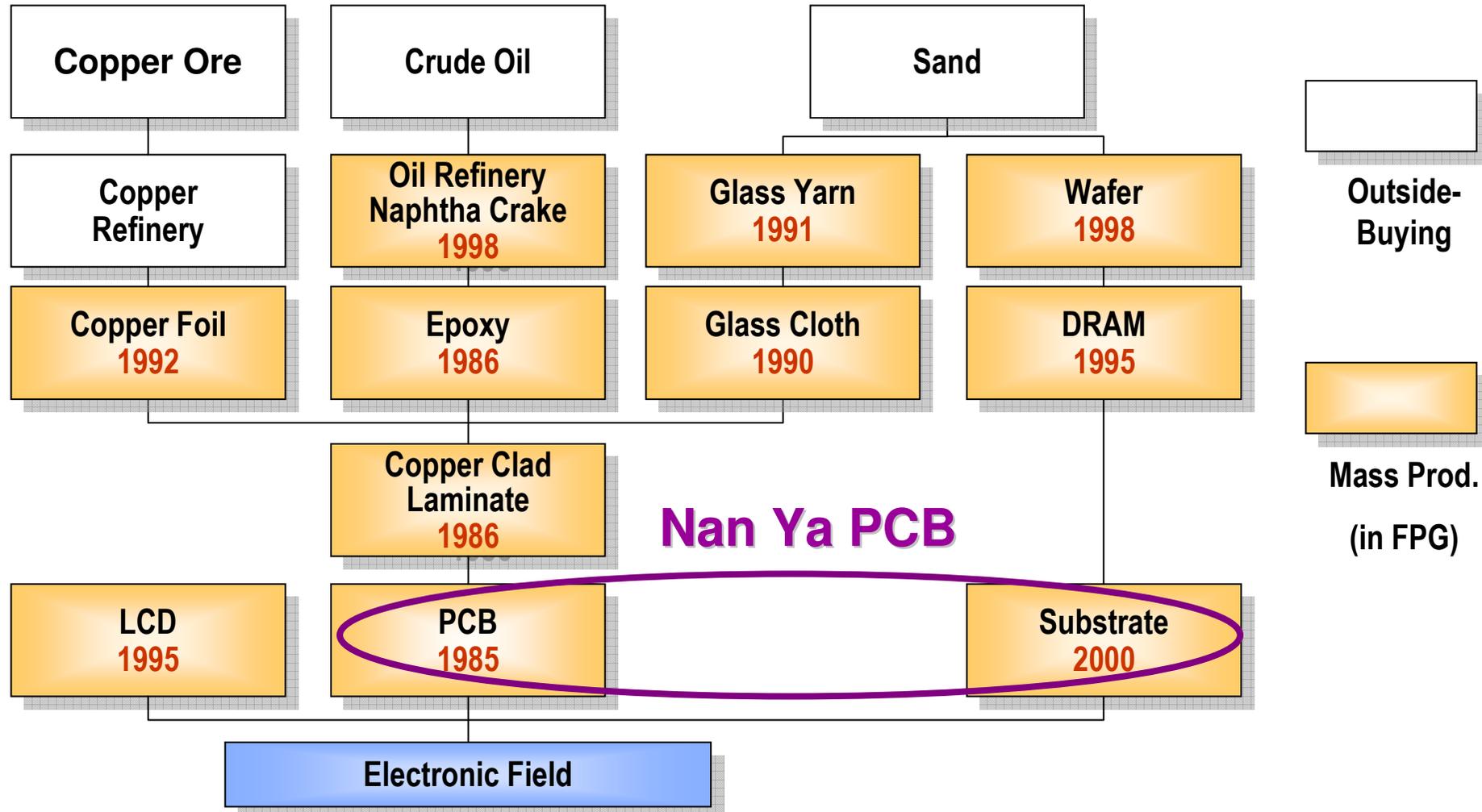


Safe Harbor Notice

- Nan Ya PCB's statements of its current expectations are forward-looking statements subject to significant risks and uncertainties and actual results may differ materially from those contained in the forward-looking statements.
- Except as required by law, we undertake no obligation to update any forward-looking statement, whether as a result of new information, future events, or otherwise.



Vertical Integration within FPG



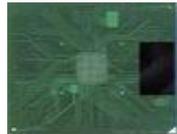


Milestone

- | | |
|-----------|--|
| Year 1985 | Start up PCB mass production |
| Year 1997 | Establish Na Ya PCB Corporation |
| Year 2000 | Start up Wire-bond Substrate mass production |
| Year 2001 | Start up Flip-chip Substrate mass production |
| Year 2002 | Start up Kunshan PCB mass production |
| Year 2006 | IPO (TWSE Ticker No.: 8046) |



Production Allocation and Monthly Capacity



Flip Chip



P1+P5 15M Units



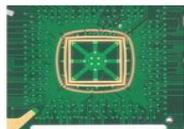
P6 8M Units



P7 7M Units



P8 (Potential 15M Units)



Wire Bond



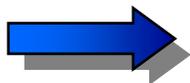
P2 320KSF



KS P2 225KSF



PCB, HDI



P3 160KSF



KS P1 1.2MSF

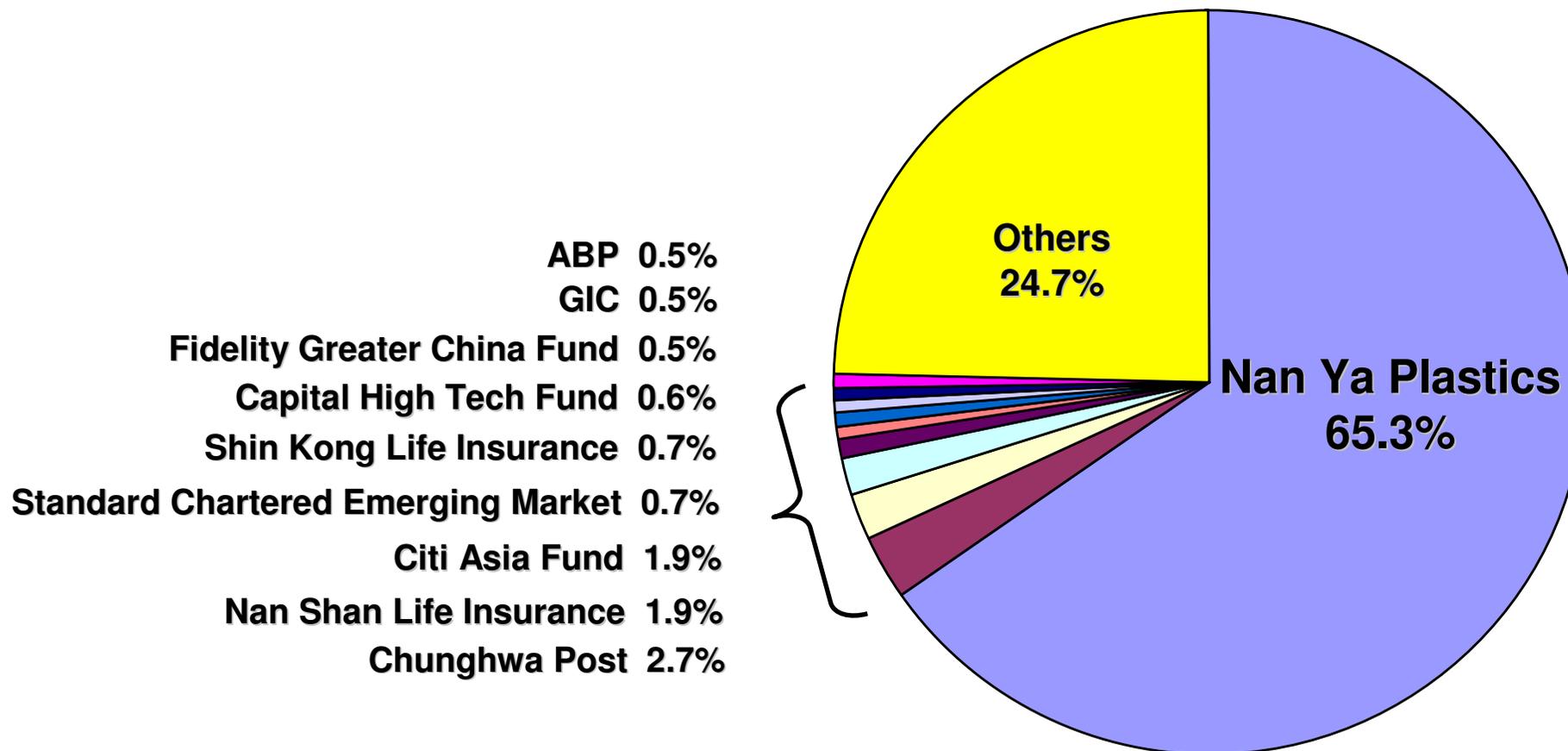


KS P2 600KSF

Note: KS=Kunshan China manufacturing campus



Structure of Shareholders

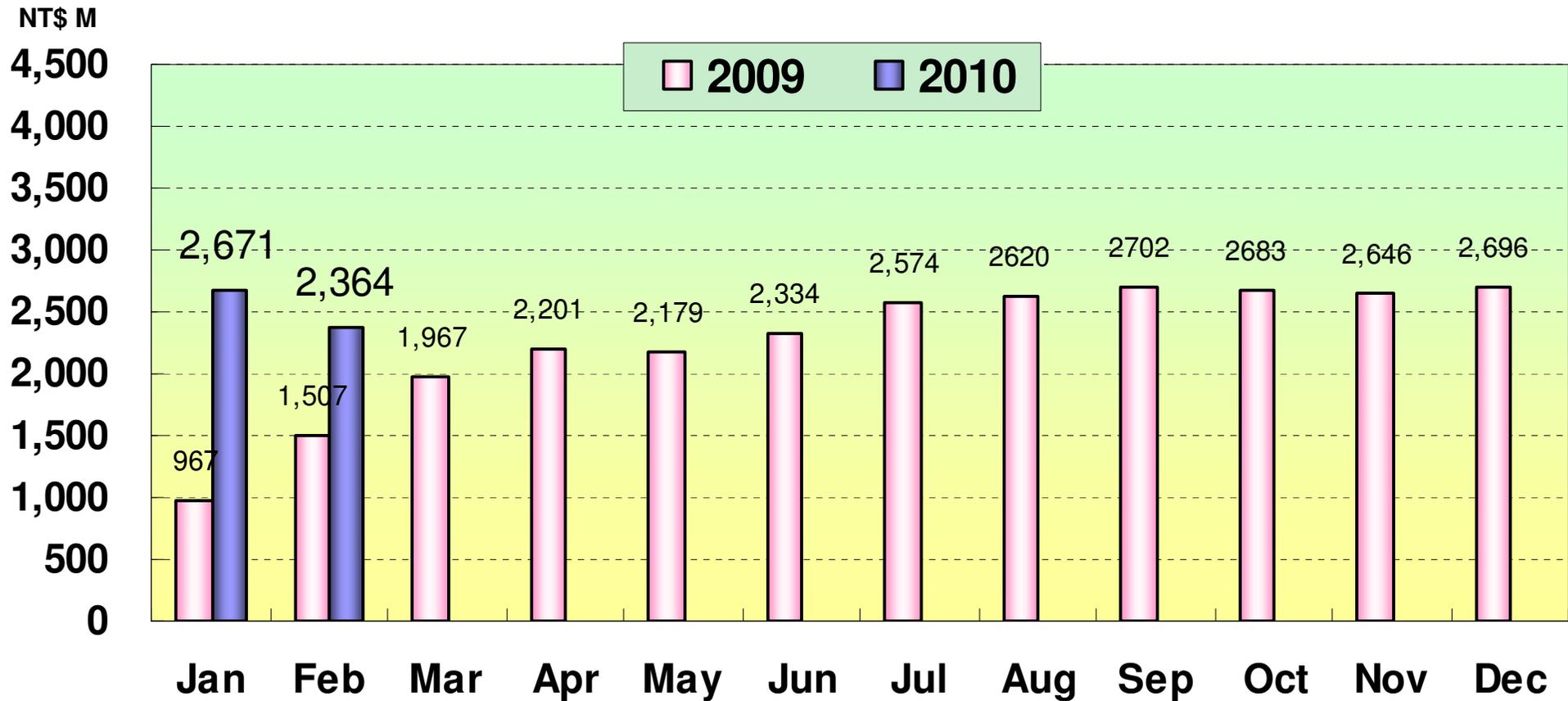


Total Shares: 630,398,531 shares

Date: August, 2009



2009~2010 Monthly Revenue in Taiwan

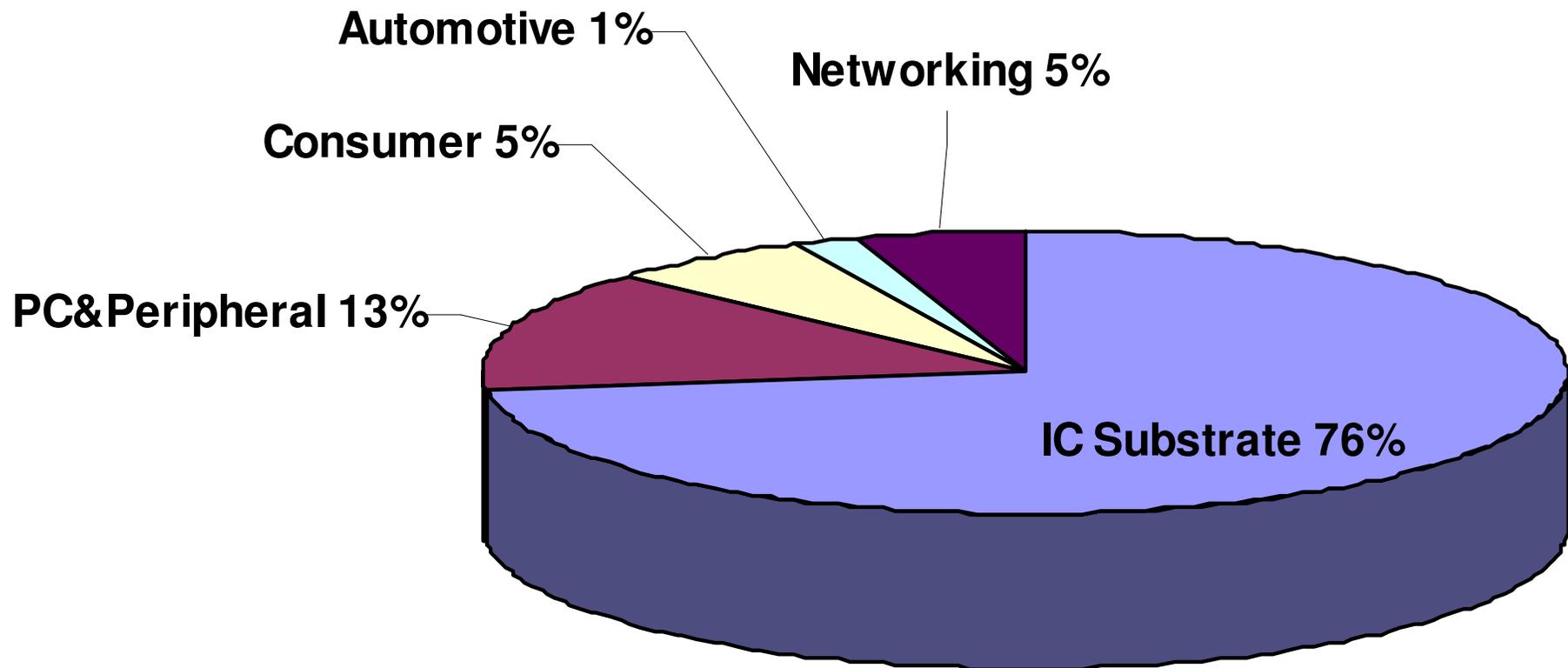


➤ 2010 February Sales = NT\$ 2.4 Billion ; MoM=-11.5% ; YoY=+56.8%

➤ Accumulated 2010 Sales = NT\$ 5.0 Billion ; YoY=+103.5%



2010 Sales Breakdown by Application





Products & Applications-PC



**GPU
FC-BGA**



**Conventional PCB
PC Motherboard**



**South Bridge
WB-BGA**



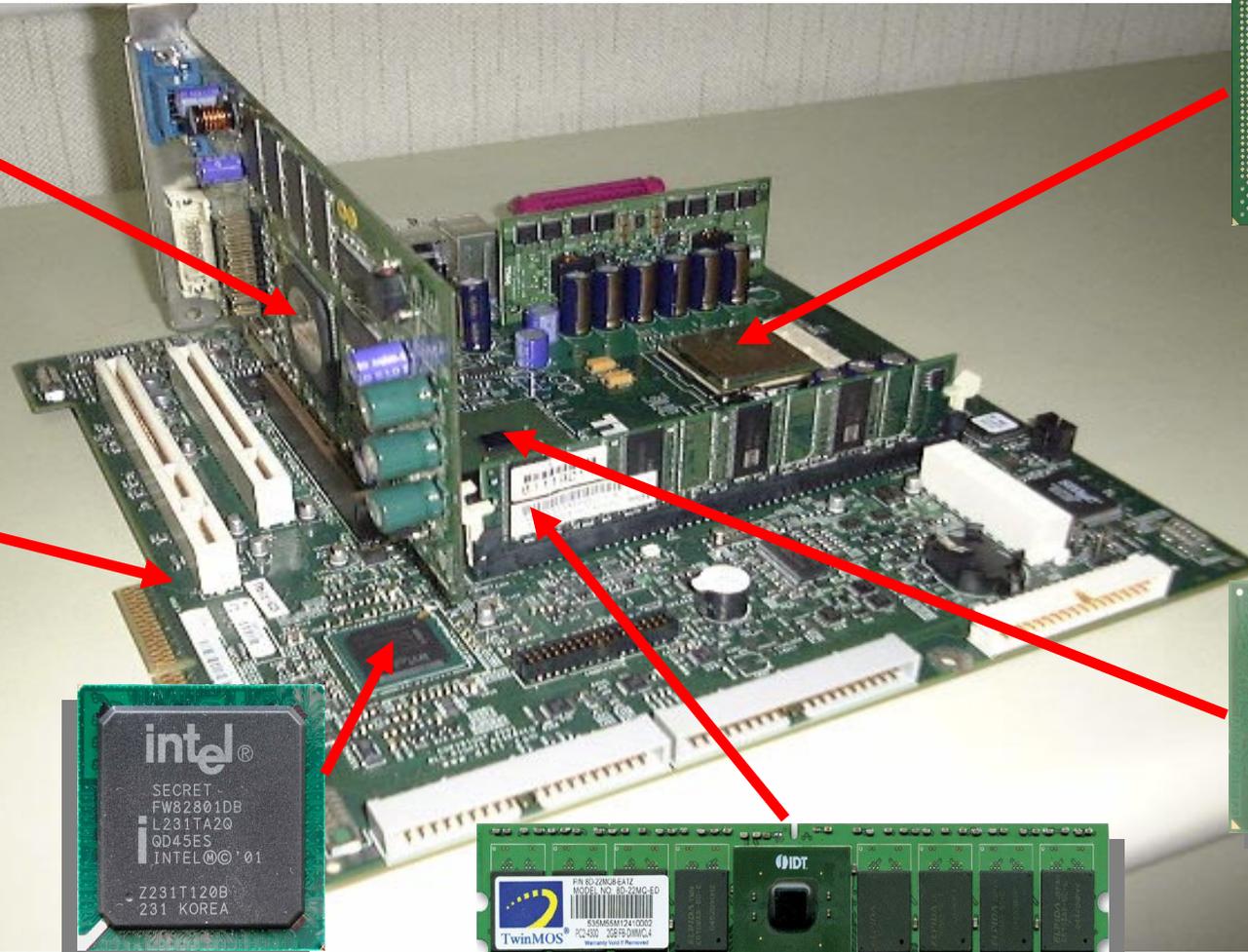
DDR II DRAM Module



**CPU
FC-PGA**



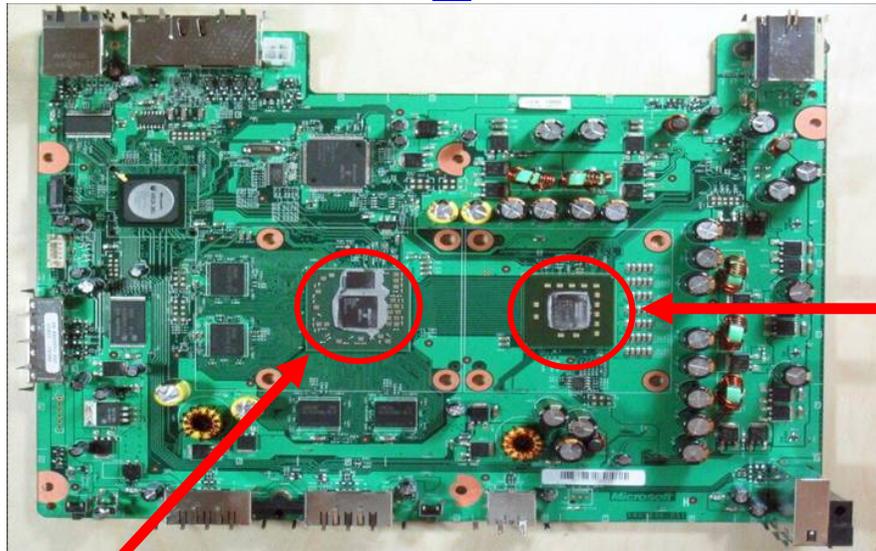
**North Bridge
FC-BGA**





Products & Applications-Game Console

A

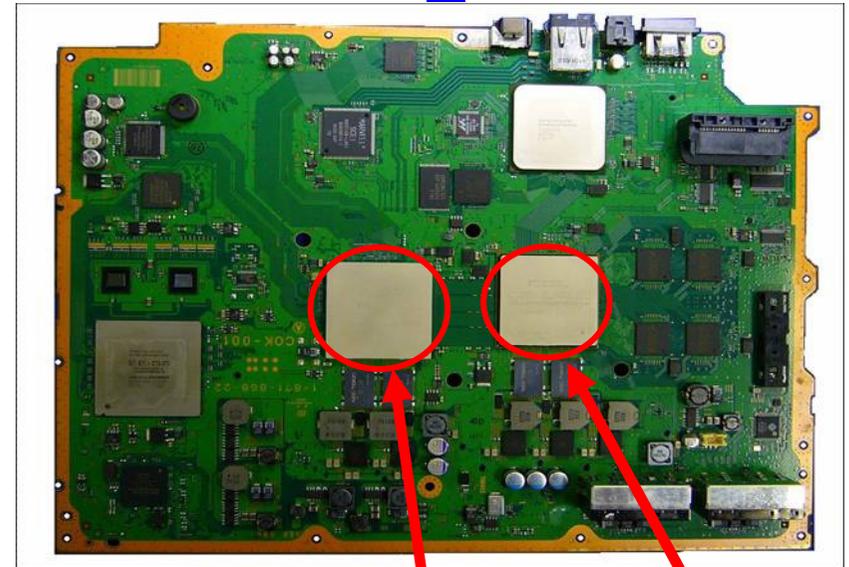


**MPU
FC-BGA
27mm²
2/2/2**

**Integrated Chipset
FC-BGA
35mm²
3/2/3**

**MPU
FC-BGA
21mm²
2/2/2**

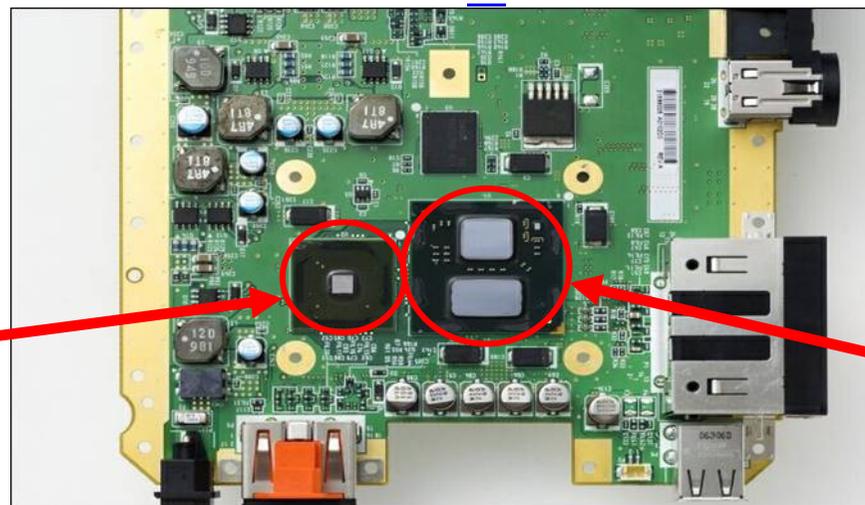
B



**Integrated Chipset
FC-BGA
42.5mm²
3/2/3**

**MPU
FC-BGA
33mm²
2/2/2**

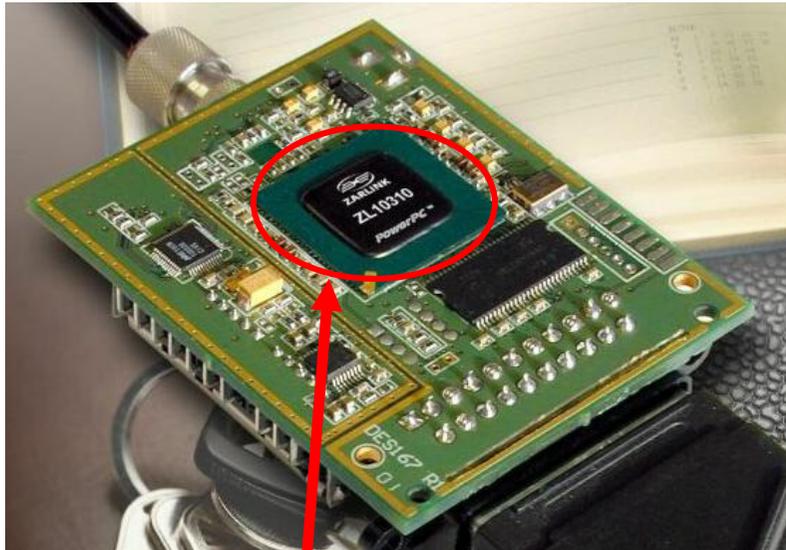
C



**Integrated Chipset
FC-BGA
31mm²
2/2/2**



Products & Applications-Others



Set-Top Box
37.5mm²
2/2/2



HDTV Chipset
35mm²
2/2/2

➤ Set-Top Box and HDTV started to migrate to flip-chip design in 2007, and annual demand has been expected to increase dramatically in 2010 .



Thank You

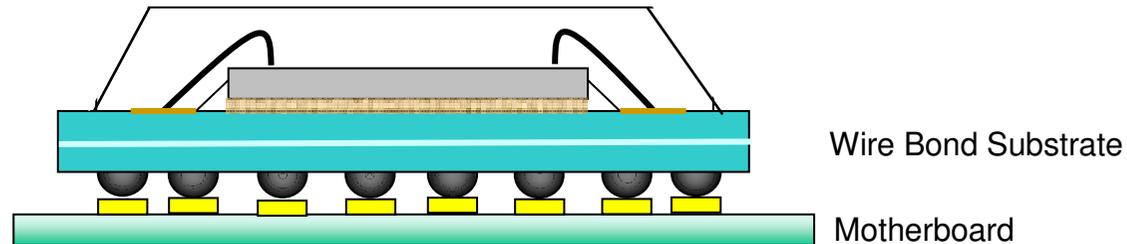
Q & A



IC Substrate Introduction

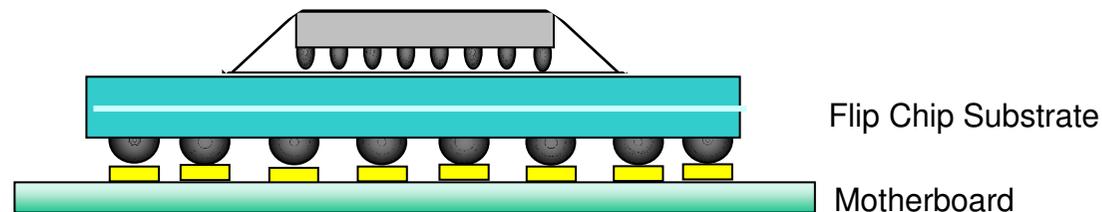
Wire Bonding Substrate Outline :

By using gold wires to connect electrical pads with the so-call wire bonding substrate which plays the function as the buffer between chips and motherboard.



Flip Chip Substrate Outline :

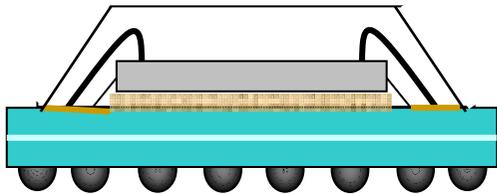
The die is directly attached to the substrate which plays as the connections between the chip and motherboard by using solder bumps rather than gold wires.



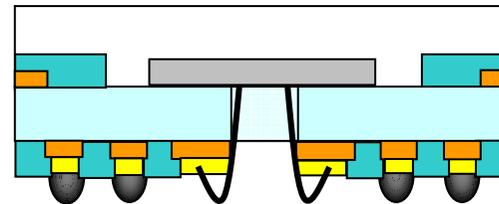


Types of Wire Bonding Substrates

•Ball Grid Array (BGA)

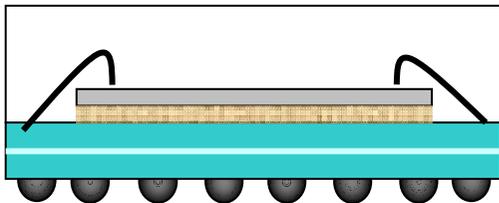


Plastic Ball Grid Array (PBGA)

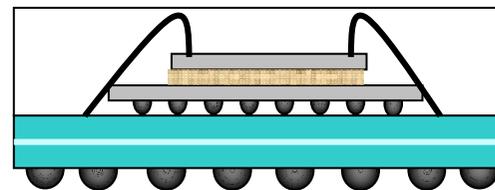


Window BGA

•Chip Scale Package (CSP)



Wire Bonding CSP (WB-CSP)

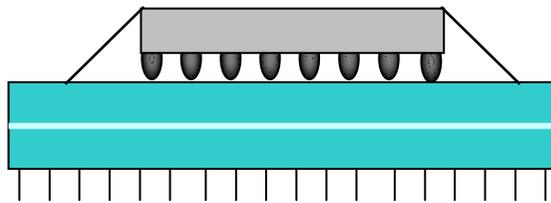


Flip Chip CSP (FC-CSP)

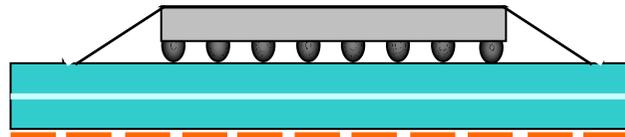


Types of Flip Chip Substrates

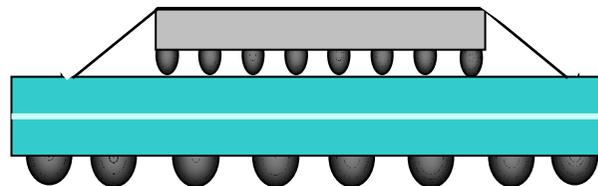
•FC-Pin Grid Array (FC-PGA)



•FC-Land Grid Array (FC-LGA)



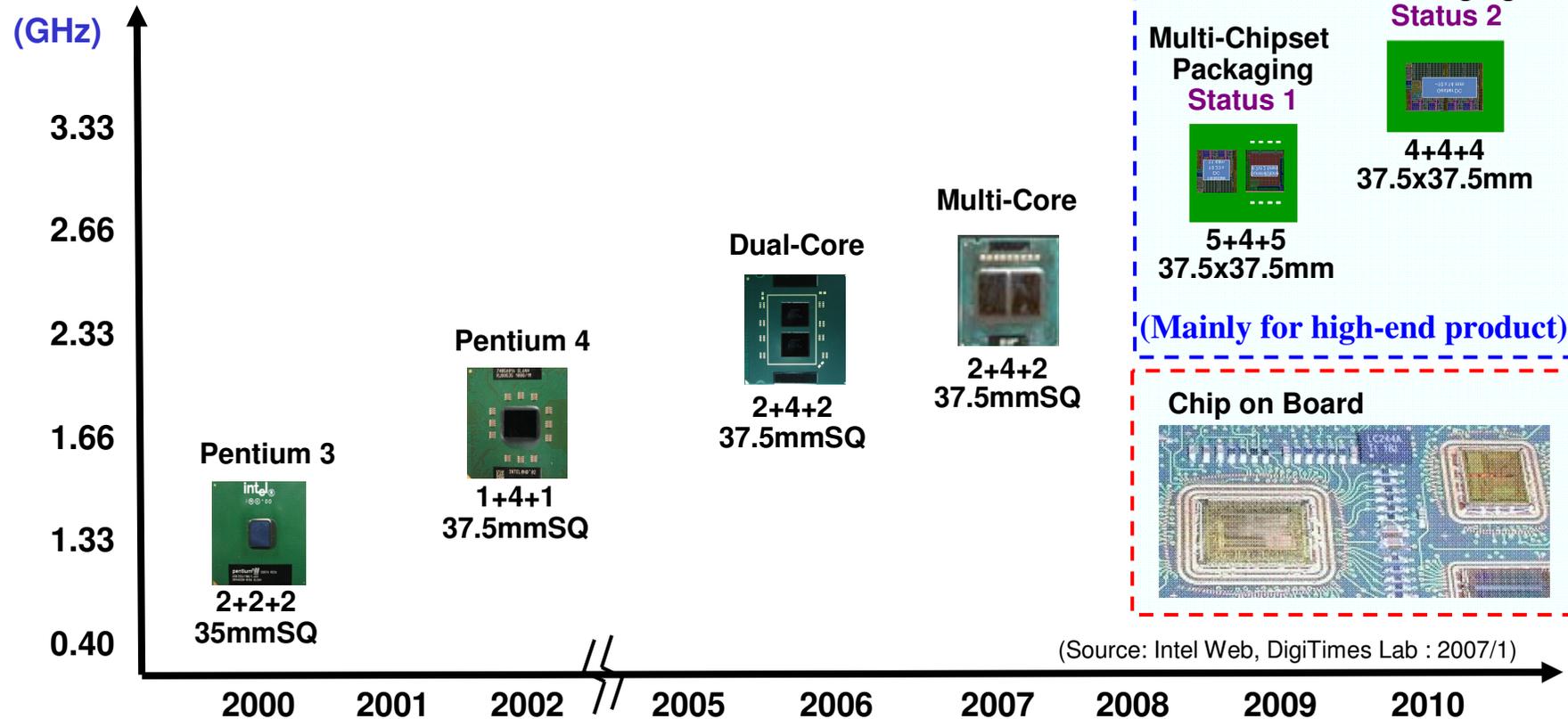
•FC-Ball Grid Array (FC-BGA)





FC Substrate Development Trend

➤ CPU Development



Wafer Process Technology Evolution (Nanometer)

